

INFORMATION DISCLOSURE CITATION IN AN APPLICATION						ATTY. DOCKET NO. 43876-155		SERIAL NO. Continuation of Serial No. 10/646,787	
(PTO-1449)						APPLICANT Craig HANSEN et al			
						FILING DATE Jan. 15, 2004		GROUP To be assigned	
U.S. PATENT DOCUMENTS									
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Codes (if known)		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear			
125 I 125		US	5,819,101	10/6/1998	Alexander Peleg, et al				
		US	5,881,275	3/9/1999	Alexander Peleg, et al				
		US	6,119,216	9/12/2000	Alexander Peleg, et al				
		US	6,516,406	2/4/2003	Alexander Peleg, et al				
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		US	6,574,724	6/3/2003	David Hoyle, et al				
		US	6,631,389 B2	10/7/2003	Derrick Chu Lin, et al				
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						Yes	No		
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)									
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.							
		EXAMINER		DATE CONSIDERED					
		Henry J. Han		8/7/06					

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U.S. PATENT DOCUMENTS						
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	4,701,875	10/20/87	Konishi, et al.			
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	Microprocessor Report, Volume 7 Number 13, October 4, 1993, IBM Regains Performance Lead with Power2, Six Way Superscalar CPU in MCM Achieves 126 SPECint92.					
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EXAMINER	Henry Lee			DATE CONSIDERED 8/17/06		

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	5,408,581	04/18/95	Suzuki, et al.			
	5,423,051	06/06/95	Fuller, et al.			
	5,426,600	06/20/95	Nakagawa, et al.			
	5,500,811	03/19/96	Corry			
	5,557,724	09/17/96	Sampat, et al.			
	5,588,152	12/24/96	Dapp, et al.			
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	5,640,543	06/17/97	Farrell, et al.			
	5,642,306	06/24/97	Mennemeier, et al.			
	5,666,298	09/09/97	Peleg, et al.			
	5,669,010	09/16/97	Duluk, Jr.			
	5,673,407	09/30/97	Poland, et al.			
	5,675,526	10/07/97	Peleg, et al.			
	5,680,338	10/21/97	Agarwal, et al.			

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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)	
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	A General-Purpose Array Processor for Seismic Processing, Nov-Dec., 1984, Volume 1, No. 3) Revisiting past digital signal processor technology, Don Shaver- Jan-Mar. 1998, 5-26
	Ruby B. Lee, "Accelerating Multimedia with Enhanced Microprocessors", IEEE Micro, April 1995, 22-32.

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HANSEN, et al.

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August 25, 2003

GROUP

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	5,500,811	03/19/96	Corry			
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	5,666,298	09/09/97	Peleg, et al.			
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	5,675,526	10/07/97	Peleg, et al.			
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<i>Henry Tran</i>	8/7/06

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SHEET 1 OF 1

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EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
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		IBM, "The PowerPC Architecture: A Specification For A New Family of Risc Processors", 2nd Ed., Morgan Kaufmann Publishers, Inc., (1994).
		Hewlett-Packard Co., "PA-RISC 1.1 Architecture and Instruction Set", Manual Part No. 09740-90039, (1990).
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